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APR. 21. 2004 9:46AM

NO. 596_ P. 1/5

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PAGE 1/5 * RCVD AT 4/21/2004 9:39:04 AM [Eastern Daylight Time] * SVR:USPTO-EFXRF-2/4 * DNIS:7469195 * CSID: * DURATION (mm-ss):01-34

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
MAGNAVACCA ET AL.

Serial No. 10/727,150

Confirmation No. 5303

Filing Date: DECEMBER 3, 2003

For: MEMORY DEVICE COMPOSED OF A
PLURALITY OF MEMORY CHIPS IN
A SINGLE PACKAGE

) Attorney Docket No. 53419

) Art Unit: 2818

REQUEST FOR CORRECTION TO THE FILING RECEIPT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

The filing receipt for the subject patent application has been received and reviewed for accuracy. A discrepancy has been discovered. The filing receipt in error lists the last name of the third inventor as "Mastroiani". The correct spelling of the inventors last name is Mastroianni.

In view of the error noted, it is respectfully requested that a corrected filing receipt be issued in this application. If any fee is believed to be necessary, authorization is given to charge Deposit Account No. 01-0484.

Respectfully submitted,



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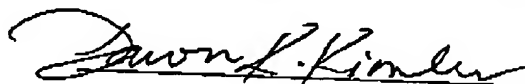
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NO. 596 . .P. 3/5

In re Patent Application of
MAGNAVACCA ET AL.
Serial No. 10/727/150
Confirmation No. 5303
Filed: DECEMBER 3, 2003

CERTIFICATE OF FACSIMILE

I HEREBY CERTIFY that this REQUEST FOR CORRECTION TO
THE FILING RECEIPT for U.S. Serial No. 10/727,150 is being
submitted via telefacsimile no. 703-746-9195 to: OFFICE OF
INITIAL PATENT EXAMINATION'S FILING RECEIPT CORRECTIONS, P.O.
BOX 1450 ALEXANDRIA, VA 22313-1450, this 21st day of April,
2004.





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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/727,150	12/03/2003	2818	900	02AG226(53419)	4	13	3

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CONFIRMATION NO. 5303

UPDATED FILING RECEIPT

100000000012263673

Date Mailed: 04/02/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Assignment For Published Patent Application

STMicroelectronics S.r.l., Agrate Brianza (MI), ITALY;

Domestic Priority data as claimed by applicant

Foreign Applications

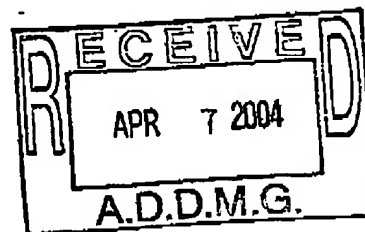
ITALY VA2002A000067 12/04/2002

If Required, Foreign Filing License Granted: 03/04/2004

Projected Publication Date: 07/15/2004

Non-Publication Request: No

Early Publication Request: No



Title

Memory device composed of a plurality of memory chips in a single package

Preliminary Class

365

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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